

XA-10062
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hisakazu DATE

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT

* * *

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-
identified patent application as indicated below.

Amendments to the Claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks begin on page 11 of this paper.